Partial list of articles, courses, etc. (in reverse chronological order)  

**Books**


**Journal Papers, Proceedings, Presentations, Articles**


“Using semiconductor electronics at cryogenic temperatures,” for meeting of the *Cryogenic Society of America*, 17 May 2001, San Jose, California. [presentation]

“Temperature as a design parameter – taken at the extremes, low and high,” for meeting of the *IEEE/Components, Packaging, and Manufacturing Technology Society*, 9 May 2001, Sunnyvale California. [presentation]


“Recent studies on low-temperature electronics,” 27 August 1998, at Communications Research Laboratory, Koganei, Japan. [presentation]


“Possibilities for the ideal cryogenic transistor,” 25 Jan. 1996 at the Physics Dept., Univ. of Milan, Milan, Italy. [presentation]


“Gravity Probe B, the Relativity Mission and cryogenic electronics for the star-tracking telescope,” 5 Sept. 1994 at the Physics Dept., Univ. of Milan, Milan, Italy. [presentation]


“Purple plague” at meeting of ISHM (International Society for Hybrid Microelectronics), Singapore Chapter, 2 June 1988, Singapore. [presentation]

“Superconductivity” Joint IEEE (Power Chapter)/IES Technical Talk, 5 May 1988, Singapore. [presentation]


“Extreme temperature electronics” IEEE Electronics Manufacturing Technology Series, 19 March 1987, Menlo Park, California. [presentation]

“Materials issues in high temperature electronics” High-Temperature Well-Logging Instrumentation Symposium, 13-14 Nov. 1985, Los Alamos, New Mexico. [presentation]


**Short Courses and Tutorials**

“Interface Electronics for Smart Sensors in Low- and High-Temperature Environments,” *Sensors Expo*, May 2000, Anaheim, California. [half-day short course]

“Superconducting Materials and Technology” *NEPCON West 88*, 23 Feb. 1988, Anaheim, California. [full-day professional advancement course]

Semester course on superconducting materials and technology, Fall 1987 and Fall 1988 at Santa Clara University.

“Low Temperature Electronics — What, Why and Whence?” *WESCON 87*, 17 Nov. 1987, San Francisco, California. [full-day tutorial]

“Electronic Devices and Technology for High Temperatures” *NEPCON West*, 25-26 Feb. 1986, Anaheim, California. [professional advancement courses in two half-day sessions]

“A System for More Efficient Development of Microwave Hybrid Components” presented at *Advances in Microwave Integrated Circuit Design and Fabrication*, IEEE Microwave Theory and Techniques Society, Santa Clara Valley Chapter, 23 March 1985, Palo Alto, California. [approx. one-hour tutorial as part of full-day short course]


“Electronics for Extreme Temperatures” University of California (Berkeley) Extension, 13 June 1984 and 16 July 1985, Santa Clara, California. [full-day short courses]