



## **Mark I. Montrose**



Mark Montrose is principle consultant of Montrose Compliance Services, Inc., a full service regulatory compliance firm specializing in electromagnetic compatibility (EMC) and industrial product safety. Prior to becoming a consultant, Mark was responsible for regulatory compliance at several high technology companies in California. He has over thirty two years of experience as a trainer, consultant, systems designer, product engineer, manufacturing engineer and component engineer in addition to management positions in both Regulatory Compliance and Engineering Services. Work experience includes design, test and certification of Information Technology, and Industrial, Scientific and Medical Equipment, specializing in the international arena for the European EMC Directive as well as product safety assessment to SEMI and CE requirements for industrial products.

Consulting services include design analysis at the circuit and component level, as well as system integration. Testing, troubleshooting and debugging difficult EMC problems are a primary area of expertise. He has also worked as an expert witness on patent infringements as well as providing technical explanations on how and why electrical products function in a simplistic manner to help lawyers understand complex engineering concepts, which in turns makes them more effective.

### **Education**

California Polytechnic State University (1979), San Luis Obispo, California

- Bachelor Degree - Electrical Engineering
- Bachelor Degree - Computer Science

University of Santa Clara (1983), Santa Clara, California

- Master Degree - Engineering Management

**Professional activities are within the IEEE. His involvement includes the following *abbreviated list*:**

- Senior member of the IEEE
- Past Division VI Director and *Member of the Board of Directors of the IEEE (2009-2010)*
- Current member - Board of Directors of the IEEE Electromagnetic Compatibility Society (EMCS)
- First President and Founder - IEEE Product Safety Engineering Society (PSES)

He has authored and presented numerous papers on sophisticated printed circuit board and system design concepts, theory, and layout related to EMC and signal integrity at IEEE International EMC Symposiums and Colloquiums worldwide. He also presents at non-IEEE International conferences as an invited speaker. Mark is an instructor for the University of California, Santa Cruz campus extension program and Underwriters Laboratory's Knowledge Service (a.k.a. UL University) in addition to commercial operations in Asia.

Mr. Montrose authored the following best-selling reference/textbook, published by Wiley/IEEE Press. All books are published under the sponsorship of the IEEE EMC Society with translation into Chinese, Japanese and Korean.

- ***Printed Circuit Board Design Techniques for EMC Compliance***, 1<sup>st</sup> ed.–1995, 2<sup>nd</sup> ed.–2000.
- ***EMC and the Printed Circuit Board - Design, Theory and Layout Made Simple***, 1999.
- ***Testing for EMC Compliance - Approaches and Technique***, 2004.
- ***EMC Made Simple***, 2013. To be released.
- ***Electronics Packaging Handbook***. Contributing Author - Chapter 6. IEEE Press/CRC Press, 2000.