

# EMMANUEL COFFY, ESQ.

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**ADMISSIONS:** Admitted in the State of New Jersey, the United States District Court of New Jersey and the Court of Appeals for the Second Circuit.  
Admitted before the United States Patent and Trademark Office (Reg. No. 63,615).

**INVENTION:** "Low friction Apparatus" U.S. Pat. No. 8,585,092.

**EXPERIENCE:** **COFFYLAW, LLC Intellectual Property & Business Law Attorneys** • [www.coffylaw.com](http://www.coffylaw.com)  
**Managing Member** May 2013-Present Freehold, NJ  
Perform the duties incumbent upon the manager of the firm. Additional duties include litigation support, conduct expert interviews associated with litigation, patent infringement assessment including complaint drafting, claim construction and infringement chart construction, patentability opinion letter, cease-and-desist letter, demand letter, due diligence; Draft and prosecute new utility (including Business Method) and design patent applications, analyze Office Action in view of prior art, draft analysis letter and prepare reply to Office Action based on client's response and instructions and conduct Examiner's interview. Draft Appeal Briefs. Negotiate IP terms of agreement with marketing vendors in the context of a patent's monetization.

**Patterson & Sheridan/Wall & Tong, LLP**  
**Associate Intellectual Property Attorney**, Oct. 2008-May 2013 Eatontown, NJ

Draft and prosecute new utility and design patent applications, analyze Office Action in view of prior art, draft analysis letter and prepare reply to Office Action based on client's response and instructions. Draft appeal briefs. Lead major Reexam action. Counsel new clients in obtaining patents/trademarks to protect their innovations. Area of concentration include wireless communications network, Internet Protocol (IP) networks, Optical Transport Networks (OTN).

**Cozen O'Connor**, Philadelphia, PA  
**Associate Intellectual Property Attorney**, Jan. 2008- Sept. 2008

Duties included litigation support, conduct expert interviews associated with litigation, patent infringement assessment including complaint drafting, claim construction and infringement chart construction, patentability opinion letter, cease-and-desist letter, demand letter, due diligence; Drafted and prosecuted new utility (including Business Method) and design patent applications, analyzed Office Action in view of prior art, drafted analysis letter and prepared reply to Office Action based on client's response and instructions. Drafted Appeal Briefs.

**THE FARRELL LAW FIRM, PC**, Uniondale, NY  
**Associate Patent Attorney**, 2006-2008

Duties included analyze Office Action in view of prior art, draft analysis letter and prepared reply to Office Action based on client's response and instructions. Prepared and filed new patent applications with USPTO. Drafted appeal briefs.

**United States Patent and Trademark Office (USPTO)** Alexandria, VA  
**Patent Examiner**, 2004 – 2006

Successfully completed the USPTO Patent Academy. Examined and prosecuted for allowance patent applications dealing primarily with computer network and Internet in the Computer Networks Technology Center (TC 2157). Conducted interviews with practitioners and prepared response to amendments and appeals. Received performance award and recognized for quality examination practice.

**ENGINEERING EXPERIENCE:**

**Honeywell Corporation, Patent Attorney Trainee,** 2002–2003 Morristown, NJ

Duties included: Assessing the strength and validity of individual patents, Conducting advanced patent searches to determine the new scope of claims; Performing infringement and validity studies, Preparing patent infringement charts and drafting patent infringement opinions.

**Lucent Technologies/Avaya Communications** Holmdel, NJ  
Member of Technical Staff (MTS), 2000–2002

Spearheaded the development effort, from inception through production phases of Avaya's state-of-the art business telephone set for use with private branch exchanges (PBX). Researched, selected and designed the latest "Flash" technology into the telephone set. Supervised and coordinated work efforts of internal design, testing and manufacturing teams, as well as marketing, regulatory and legal experts. Traveled to Mexico to coordinate and supervise the initial production run.

**L3/Honeywell/Allied Signal** Teterboro, NJ  
Digital Design Engineer, 1996 - 2000

Lead Design Engineer at world aerospace leader in charge of the development effort to produce a Digital Signal Processing Circuit Card for a Fiber Optic Gyroscope based Inertia Measurement Unit (IMU) for use in conjunction with Global Positioning Satellite (GPS) in the Navy's Guided Munition.

**Square D Company** Raleigh, NC  
Senior Hardware Design Engineer, 1994 - 1996

Designed and developed programmable logic controllers.

**Computer Sciences Raytheon** Kennedy Space Center, FL  
Systems Network Engineer, 1990 - 1994

Performed analysis and top-level design of mission-critical, secured computer systems networks at Cape Canaveral (Kennedy Space Center, NASA) for use in orbital missions.

**EDUCATION:**

**Seton Hall University School of Law** Newark, NJ  
J.D., June 2003 (worked full-time while attending evening program)  
Concentration: Intellectual Property

**Stevens Institute of Technology** Hoboken, NJ  
PH.D. Technology Management, Aug 1998  
(Admitted as non-matriculated and later accepted as PH.D. candidate)

**Florida Institute of Technology** Melbourne, FL  
M.S., Electrical Engineering, June 1993 Specialization: Microelectronics (Semiconductor)

**Bridgeport Engineering Institute** Bridgeport, CT  
B.S., Electrical Engineering, October 1986.

**LANGUAGES:  
BOOK/ARTICLE**

French – Read, write and speak fluently; Spanish – Read and write proficiently and Creole.  
Co-author: "The Value of Your Idea\$" published by Trafford and "The Need for Signal Claims."